



Low ON-Resistance, Low Voltage, SPDT Analog Switch

1 FEATURES

-3dB Bandwidth: 120MHzHigh Speed, Typically 29ns

Supply Range: +1.8V to +5.5V

• Low ON-State Resistance, 3.0Ω(TYP)

• Break-Before-Make Switching

• Rail-to-Rail Operation

TTL/CMOS Compatible

• Micro Size Package: SC70-6

Extended Industrial Temperature Range:
 -40°C to +125°C

2 APPLICATIONS

- Wearable Devices
- Battery-Operated Equipment
- Signal Gating, Chopping, Modulation or Demodulation (Modem)
- Portable Computing
- Cell Phones

3 DESCRIPTIONS

The RES4624 is a lowon-resistance, single-pole double-throw (SPDT) analog switch that is designed to operate from 1.8 V to 5.5 V.

The RES4624 device can handle both analog and gi tal signals. It features fast switching speeds (t_{ON} = 29ns, t_{OFF} = 17ns) and low on-resistance (3.0 Ω TYP).

These features make this device suitable for a wide variety of portable applications including cell phones, audio devices, and instrumentation.

Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog-to-digital and digital-to-analog conversion systems.

Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RES4624ISR	SC70-6	2.10mm×1.25mm

For all available packages, see the orderable addendum at the end of the data sheet.



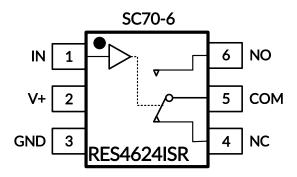
5 PACKAGE/ORDERING INFORMATION (1)

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING (2)	MSL (3)	PACKAGE OPTION	
RES4624	RES4624ISR	-40°C ~125°C	SC70-6 (4)	RES4624ISR	MSL3	Tape and Reel, 3000	

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.
- (4) Equivalent to SOT363.

6 PIN CONFIGURATIONS



6.1 Pin Description

NAME	PIN	FUNCTION
IN	1	Digital Control Pin
V+	2	Power Supply
GND	3	Ground
NC	4	Normally-Closed Terminal
СОМ	5	Common Terminal
NO	6	Normally-Open Terminal

6.2 Function Table

LOGIC	NO	NC
0	OFF	ON
1	ON	OFF



7 SPECIFICATIONS

7.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) (1)

SYMBOL	PARAMETER			MIN	MAX	UNIT
V+	Supply Voltage			-0.3	6.0	
VIN	Input Voltage			-0.3	6.0	V
	Analog, Digital Voltage Range (2)		-0.3	(V ₊)+0.3		
	Continuous Current NO, NC, or COM	Continuous Current NO, NC, or COM				Л
I PEAK	Peak Current NO, NC, or COM				+500	mA
θја	Package thermal impedance (3) SC70-6				265	°C/W
ΤJ	Junction Temperature (4)		-40	150	°C.	
T _{stg}	Storage temperature	_		-65	+150]

⁽¹⁾ Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

7.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
M	Flooting displaying	Human-Body Model (HBM)	±3000	٧
V _(ESD)	Electrostatic discharge	Machine Model (MM)	±200	V



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

7.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

SYMBOL	PARAMETER	MIN	MAX	UNIT
Vcc	Supply Voltage	1.8	5.5	V
TA	Operating temperature	-40	+125	°C

⁽²⁾ Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.3V beyond the supply rails should be current-limited to 10mA or less.

⁽³⁾ The package thermal impedance is calculated in accordance with JESD-51.

⁽⁴⁾ The maximum power dissipation is a function of $T_{J(MAX)}$, $R_{\theta JA}$, and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A) / R_{\theta JA}$. All numbers apply for packages soldered directly onto a PCB.



7.4 Electrical Characteristics

V+ = 5.0 V, $T_A= -40^{\circ}\text{C}$ to 125°C (unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	V+	TA	MIN ⁽²⁾	TYP (3)	MAX ⁽²⁾	UNIT
ANALOG SWITCH				•				
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}			FULL	0		V+	V
On-Resistance On-Resistance Match			5 \/	+25°C		3.0	4.0	Ω
0 0 : 1	D	$0 \le (V_{NO} \text{ or } V_{NC}) \le V+$	5V	FULL			5.0	Ω
On-Resistance	Ron	I _{COM} = -10mA, Switch ON, See Figure 4	0.01/	+25°C		5.5	7.0	Ω
		Jee rigare r	3.3V	FULL			7.5	Ω
			5) (+25°C		0.1	0.8	Ω
On-Resistance Match		$0 \le (V_{NO} \text{ or } V_{NC}) \le V+$		FULL			0.9	Ω
Between Channels	ΔR on	I _{COM} = -10mA, Switch ON, See Figure 4	2.01	+25°C		0.1	0.8	Ω
		See Figure 4	3.3V	FULL			0.9	Ω
			5),	+25°C		0.7	0.85	Ω
	$R_{FLAT(ON)}$ $0 \le (V_{NO} \text{ or } V_{NC}) \le V+,$ $1_{COM} = -10\text{mA}, \text{ Switch ON},$ $See \text{ Figure 4}$ $3.3V$, ,	5V	FULL			0.95	Ω
On-Resistance Flatness		+25°C		2.5	3.0	Ω		
		Jee rigare r	3.3V	FULL			3.2	Ω
NC, NO OFF Leakage Current	Inc(off), Ino(off)	V _{NO} or V _{NC} = 0.3V, V+/2 V _{COM} = V+/2, 0.3V See Figure 5	1.8 to 5.5V	FULL			1	μΑ
NC, NO, COM ON Leakage Current	Inc(on), Ino(on), Icom(on)	V _{NO} or V _{NC} = 0.3V, Open V _{COM} = Open, 0.3V See Figure 6	1.8 to 5.5V	FULL			1	μΑ
DIGITAL CONTROL INF	PUTS (1)							
	.,		5V	FULL	1.5			V
Input High Voltage	V _{INH}		3.3V	FULL	1.3			٧
	.,		5V	FULL			0.6	٧
Input Low Voltage	VINL		3.3V	FULL			0.5	V
Input Leakage Current	I _{IN}	V _{IN} = V _{IO} or 0	1.8 to 5.5V	FULL			1	μΑ

⁽¹⁾ All unused digital inputs of the device must be held at V_{IO} or GND to ensure proper device operation.

⁽²⁾ Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

⁽³⁾ Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.



Electrical Characteristics (continued) V+ = 5.0 V, T_A= -40°C to 125°C (unless otherwise noted)

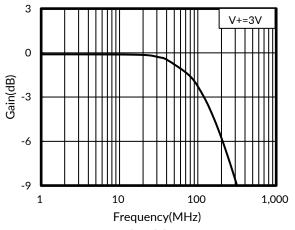
PARAMETER	SYMBOL	CONDITION	V+	TA	MIN	TYP	MAX	UNIT	
DYNAMIC CHARACTE	RISTICS						•		
T 0 T		$V_{COM} = V +, R_{L} = 300\Omega, C$	C _L = 35pF,	5V	.0506		29		
Turn-On Time	ton	See Figure 8	• • • • •		+25°C		33		ns
T O# T:	_	$V_{COM} = V +, R_L = 300\Omega, C$	C _L = 35pF,	5V	10500		17		
Turn-Off Time	t _{OFF}			3.3V	+25°C		18		ns
Break-Before-Make		$V_{NO1} = V_{NC1} = V_{NO2} = V_N$	_{C2} = 3V,	5V	.0506		10		
Time Delay	tввм	$R_L = 300\Omega$, $C_L = 35pF$, See Figure 9		3.3V	+25°C		11		ns
Off -+:	0	$R_L = 50\Omega$, Switch OFF,	f = 10MHz		+25°C		-43		dB
Off Isolation	O _{ISO}	1 3032, SWITCH OIT,	f = 1MHz		+25°C		-67		dB
-3dB Bandwidth	BW	Switch ON, $R_L = 50\Omega$ See Figure 10			+25°C		120		MHz
NC, NO OFF Capacitance	CNC(OFF), CNO(OFF)	V_{NC} or V_{NO} =V+/2 or GN OFF, See Figure 7	D, Switch		+25°C		15		pF
NC, NO, COM ON	C _{NC(ON)} , C _{NO(ON)} ,	V_{NC} or $V_{NO}=V+/2$ or GN	D, Switch		+25°C		50		рF
Capacitance	C _{COM(ON)}	ON, See Figure 7			123 C		50		Pi
POWER REQUIREMEN	NTS								
Power Supply Range	V+				FULL	1.8		5.5	V
Power Supply Current	l+	$V_{IN} = GND \text{ or } V_+$		5.5V	FULL			1	μΑ



7.5 Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

 $V+ = 5.0 \text{ V}, T_A = -40 ^{\circ}\text{C} \text{ to } 125 ^{\circ}\text{C} \text{ (unless otherwise noted)}$



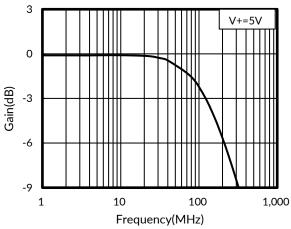


Figure 1. Bandwidth vs Frequency

Figure 2. Bandwidth vs Frequency

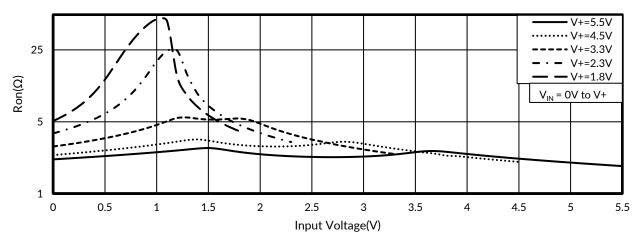


Figure 3. Typical Ron as a Function of Input Voltage



8 PARAMETER MEASUREMENT INFORMATION

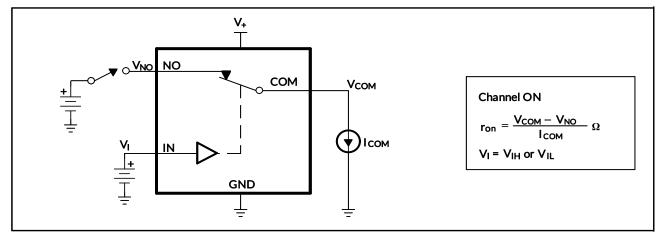


Figure 4. ON-State Resistance (Ron)

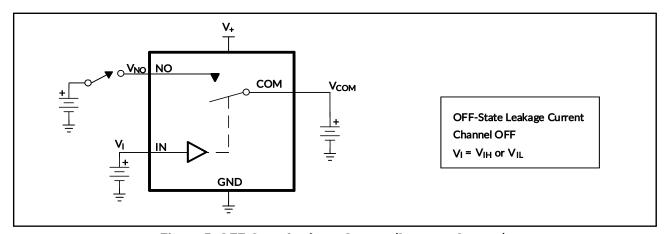


Figure 5. OFF-State Leakage Current (ICOM (OFF), INO (OFF))

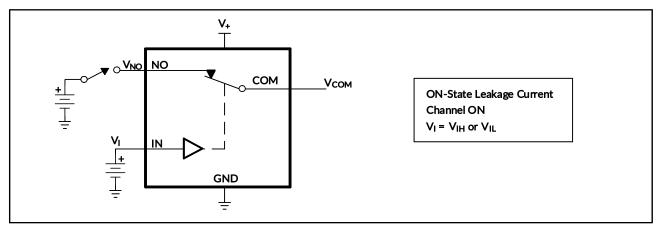


Figure 6. ON-State Leakage Current (ICOM (ON), INO (ON))



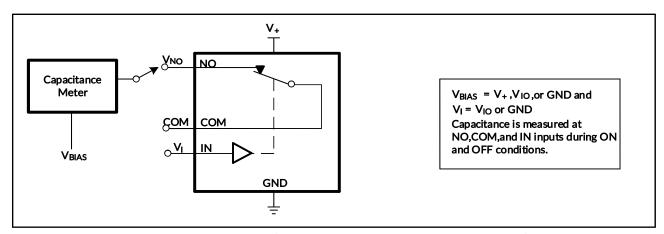


Figure 7. Capacitance (C_I, C_{COM (OFF)}, C_{COM (ON)}, C_{NO (OFF)}, C_{NO (ON)})

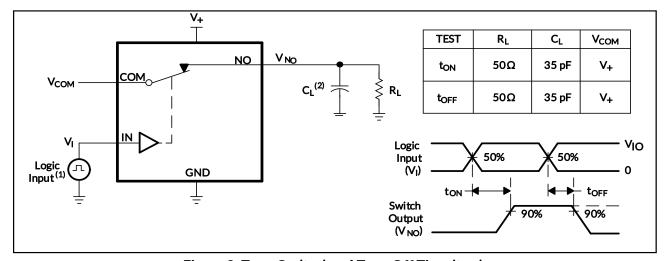


Figure 8. Turn-On (ton) and Turn-Off Time (toff)

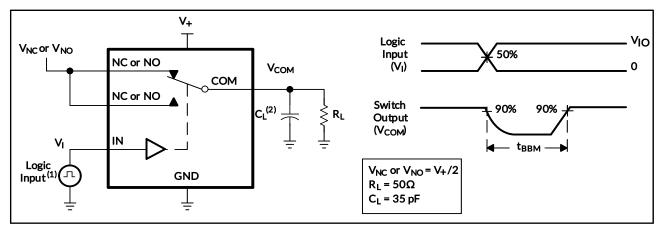


Figure 9. Break-Before-Make Time (t_{BBM})



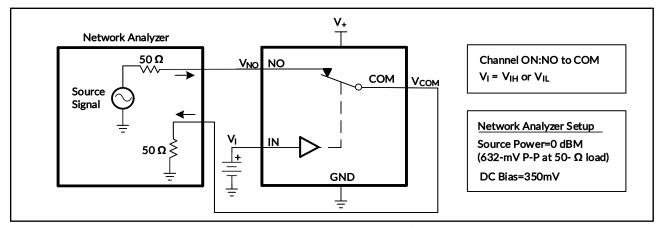


Figure 10. Bandwidth (BW)

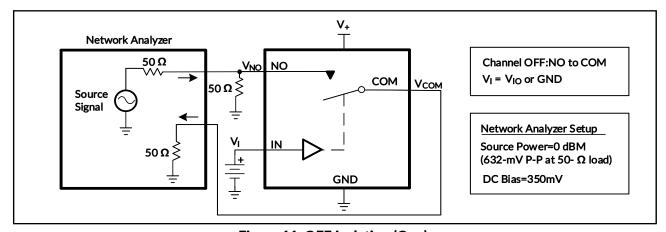


Figure 11. OFF Isolation (O_{ISO})

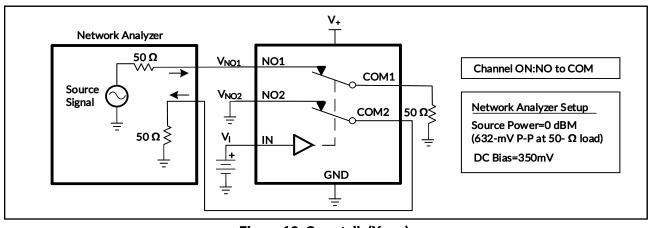


Figure 12. Crosstalk (X_{TALK})



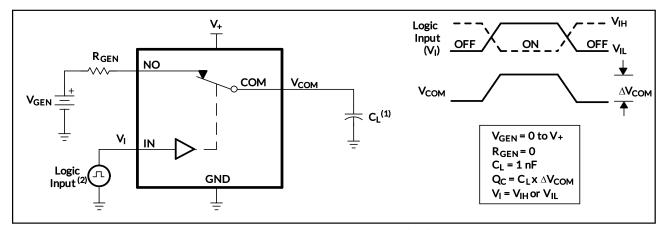


Figure 13. Charge Injection (Qc)

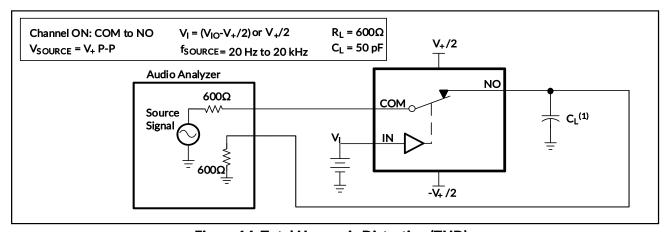
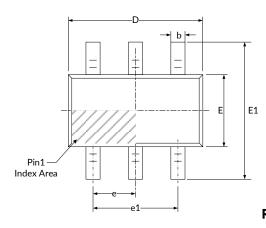
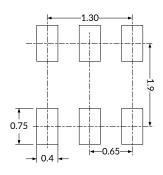


Figure 14. Total Harmonic Distortion (THD)

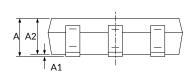


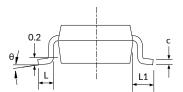
9 PACKAGE OUTLINE DIMENSIONS SC70-6 (3)





RECOMMENDED LAND PATTERN (Unit: mm)





Complete	Dimensions I	n Millimeters	Dimensions In Inches			
Symbol	Min	Max	Min	Max		
A (1)	0.900	1.100	0.035	0.043		
A1	0.000	0.100	0.000	0.004		
A2	0.900	1.000	0.035	0.039		
b	0.150	0.350	0.006	0.014		
С	0.080	0.150	0.003	0.006		
D (1)	2.000	2.200	0.079	0.087		
E (1)	1.150	1.350	0.045	0.053		
E1	2.150	2.450	0.085	0.096		
е	0.650(0.650(BSC) (2)		BSC) (2)		
e1	1.300(BSC) (2)	0.051(BSC) (2)		
L	0.260	0.460	0.010	0.018		
L1	0.5	525	0.0)21		
θ	0°	8°	0°	8°		

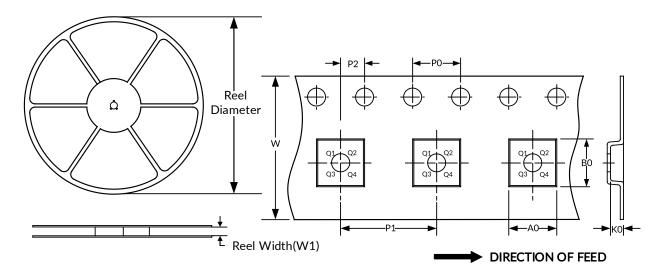
NOTE:

- 1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
- 2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
- 3. This drawing is subject to change without notice.



10 TAPE AND REEL INFORMATION REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width(mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-6	7"	9.5	2.40	2.50	1.20	4.0	4.0	2.0	8.0	Q3

NOTE:

- 1. All dimensions are nominal.
- 2. Plastic or metal protrusions of 0.15mm maximum per side are not included.