



# 1-Bit Dual-Supply Unidirectional Bus Transceiver

### 1 FEATURES

- Power-Supply Range:
   V<sub>CCA</sub> and V<sub>CCB</sub>: 1.65V to 5.5V
- V<sub>CC</sub> Isolation: If Either V<sub>CC</sub> is at GND, Both Ports are in the High-Impedance State
- Low power consumption, 4μA Max
- Output drive up to ±24mA@3.0V
- No Power-Supply Sequencing Required:
   Either V<sub>CCA</sub> or V<sub>CCB</sub> can be Ramped First
- I<sub>OFF</sub>: Supports Partial-Power-Down Mode Operation
- Extended Temperature: -40°C to +125°C

### 2 APPLICATIONS

- Industrial
- Enterprise
- Telecom, such as VOIP
- Personal electronic

### **3 DESCRIPTIONS**

The RES74AUP1T34GW is 1-bit non-inverting bus transceiver uses two separate configurable power supply rails. The A port is designed to track  $V_{\text{CCA}}$ , which supporting operating voltages from 1.65V to 5.5V, and the B port supporting operating voltages from 1.65V to 5.5V while it tracks the  $V_{\text{CCB}}$  supply. This allows for universal low-voltage unidirectional translation between any of the 1.8V, 2.5V, 3.3V and 5V voltage nodes.

The RES74AUP1T34GWis designed for asynchronous communication between two data buses. The A bus is input side, the B bus is output side.

The RES74AUP1T34GW is fully specified for partia lpo we r-down applications using  $l_{\rm off}$ . The  $l_{\rm off}$  circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

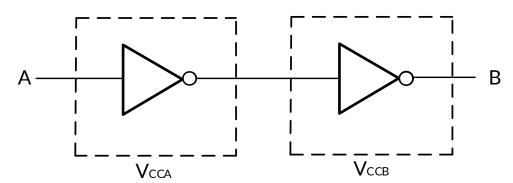
The RES74AUP1T34GW is available in Green SC70-5 packages. It operates over an ambient temperature range of -40°C to +125°C.

#### **Device Information (1)**

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RES74AUP1T34GW	SC70-5	2.10mm×1.25mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

# **4 Functional Block Diagram**





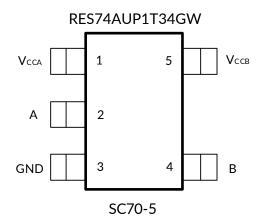
# 6 PACKAGE/ORDERING INFORMATION (1)

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING (2)	MSL (3)	PACKAGE OPTION
RES74AUP1T34	RES74AUP1T34GW	-40°C ~+125°C	SC70-5 (4)	RES74AUP1T34GW	MSL3	Tape and Reel, 3000

#### NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

# **7 PIN CONFIGURATIONS**



### 7.1 PIN DESCRIPTION

RES74AUP1T34GW	NAME	TYPE (1)	FUNCTION
SC70-5	NAME	TYPE	FUNCTION
1	$V_{CCA}$	Р	A Port Supply Voltage.1.65V $\leq$ V <sub>CCA</sub> $\leq$ 5.5V
2	Α	I	Input A. Reference to V <sub>CCA</sub> .
3	GND	-	Ground.
4	В	0	Output B. Reference to V <sub>CCB</sub> .
5	$V_{CCB}$	Р	B Port Supply Voltage.1.65V $\leq$ V <sub>CCB</sub> $\leq$ 5.5V.

<sup>(1)</sup> I=input, O=output, P=power.

### 7.2 Function Table (2)

INPUT (1)	ОИТРИТ
A PORT (3)	B PORT (3)
Н	Н
L	L

#### Note:

- (1) The input circuit of the data I/O is always active.
- (2) When either  $V_{CCA}$  or  $V_{CCB}$  is at GND level, the device goes into suspend mode.
- (3) H=High voltage level, L=Low voltage level.



# **8 SPECIFICATIONS**

# 8.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) (1)

SYMBOL	PARAMETER		MIN	MAX	UNIT
Vcca (3)	Supply Voltage Range	-0.5	6.5	V	
V <sub>CCB</sub> (3)	Supply Voltage Range		-0.5	6.5	V
V <sub>I</sub> <sup>(2)</sup>	Input Voltage Range	A port	-0.5	6.5	V
Vo (2)	Voltage range applied to any output in the high- impedance or power-off state	B port	-0.5	V <sub>CCB</sub> +0.5	V
lık	Input clamp current	V <sub>I</sub> <0		-50	mA
Іок	Output clamp current	Vo<0		-50	mA
lo	Continuous output current			±50	mA
	Continuous current through VCCA, VCCB or GND			±100	mA
θJA	Package thermal impedance <sup>(4)</sup>				°C/W
θJA	Package thermal impedance '"	SC70-5		376	C/VV
TJ	Junction temperature (5)		-40	150	°C
$T_{stg}$	Storage temperature		-65	+150	

<sup>(1)</sup> Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of  $V_{\text{CCA}}$  and  $V_{\text{CCB}}$  are provided in the recommended operating conditions table.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of  $T_{J(MAX)}$ ,  $R_{\theta JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_{J(MAX)} T_A) / R_{\theta JA}$ . All numbers apply for packages soldered directly onto a PCB.

## 8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
		Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 (1)	±2000	V
V(ESD)	Electrostatic discharge	Charged-device model (CDM), per ANSI/ESDA/JEDEC JS-002 (2)	±1500	V
		Machine Model (MM)	±200	V

- (1) JEDEC document JEP155 states that 500 V HBM allows safe manufacturing with a standard ESD control process.
- (2) JEDEC document JEP157 states that 250 V CDM allows safe manufacturing with a standard ESD control process.



#### **ESD SENSITIVITY CAUTION**

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.



# **8.3 Recommended Operating Conditions**

 $V_{\text{CCA}}$  is the supply voltage associated with the input port.  $V_{\text{CCB}}$  is the supply voltage associated with the output port.

PARAI	METER	V <sub>CCA</sub> (1)	V <sub>CCB</sub> <sup>(2)</sup>	MIN	TYP	MAX	UNIT	
Supply voltage (1)	Vcca			1.65		5.5	V	
Supply Voltage (-)	V <sub>CCB</sub>				V			
		1.65V to 1.95V		V <sub>CCA</sub> x 0.75				
High-level input	V <sub>IH</sub> <sup>(4)</sup>	2.3V to 2.7V		V <sub>CCA</sub> x 0.7			V	
Voltage	VIH	3V to 3.6V		V <sub>CCA</sub> x 0.7			V	
		4.5V to 5.5V		V <sub>CCA</sub> x 0.7				
		1.65V to 1.95V				V <sub>CCA</sub> x 0.35		
Low-level input	VII. (4)	2.3V to 2.7V				V <sub>CCA</sub> x 0.3	.,	
Voltage	VIL (4)	3V to 3.6V				V <sub>CCA</sub> x 0.3	V	
		4.5V to 5.5V				V <sub>CCA</sub> x 0.3		
Input voltage	Vı			0		5.5	V	
Output voltage	Vo			0		V <sub>CCB</sub>	V	
			1.65V to 1.95V			-4		
I liab laval autout a			2.3V to 2.7V			-8		
High-level output o	current (IOH)		3V to 3.6V			-24	mA	
			4.5V to 5.5V			-32		
			1.65V to 1.95V			4		
Landard and a			2.3V to 2.7V			8		
Low-level output c	urrent (IOL)		3V to 3.6V			24	mA	
			4.5V to 5.5V			32		
		1.65V to 1.95V				20		
Input transition	D-4- :4- (3)	2.3V to 2.7V				20		
rise or fall rate(Δt/Δv)	Data inputs <sup>(3)</sup>	3V to 3.6V				10	ns/V	
		4.5V to 5.5V				5	-	
T <sub>A</sub> Operating free-a	air temperature			-40		125	°C	

<sup>(1)</sup>  $V_{CCA}$  is the  $V_{CC}$  associated with the data input port.

<sup>(2)</sup>  $V_{CCB}$  is the  $V_{CC}$  associated with the output port.

<sup>(3)</sup> All unused or driven (floating) data inputs (I/Os) of the device must be held at logic HIGH or LOW (preferably V<sub>CCI</sub> or GND) to ensure proper device operation and minimize power.

<sup>(4)</sup> For  $V_{CCA}$  values not specified in the data sheet,  $V_{IH}$  min =  $V_{CCA} \times 0.7$  V,  $V_{IL}$  max =  $V_{CCA} \times 0.3$  V.



# **8.4 Electrical Characteristics**

over recommended operating free-air temperature range (unless otherwise noted) (1) (2)

PA	RAMETER	CONDITIONS	Vcca	V <sub>CCB</sub>	TEMP	MIN <sup>(3)</sup>	TYP <sup>(4)</sup>	MAX <sup>(3)</sup>	UNIT
		I <sub>OH</sub> = -100μΑ V <sub>I</sub> =V <sub>IH</sub>	1.65V to 4.5V	1.65V to 4.5V		V <sub>CCB</sub> - 0.1			
		I <sub>OH</sub> = -4mA V <sub>I</sub> =V <sub>IH</sub>	1.65V	1.65V		1.2			
Vон		I <sub>OH</sub> = -8mA V <sub>I</sub> =V <sub>IH</sub>	2.3V	2.3V		1.9			V
		I <sub>OH</sub> = -24mA V <sub>I</sub> =V <sub>IH</sub>	3V	3V		2.4			
		I <sub>OH</sub> = -32mA V <sub>I</sub> =V <sub>IH</sub>	4.5V	4.5V	Full	3.8			
		I <sub>OL</sub> = 100μΑ V <sub>I</sub> =V <sub>IL</sub>	1.65V to 4.5V	1.65V to 4.5V				0.1	
		I <sub>OL</sub> = 4mA V <sub>I</sub> =V <sub>IL</sub>	1.65V	1.65V				0.45	
$V_{OL}$		I <sub>OL</sub> = 8mA V <sub>I</sub> =V <sub>IL</sub>	2.3V	2.3V				0.3	٧
		I <sub>OL</sub> = 24mA V <sub>I</sub> =V <sub>IL</sub>	3V	3V				0.55	
		I <sub>OL</sub> = 32mA V <sub>I</sub> =V <sub>IL</sub>	4.5V	4.5V				0.55	
	Input		4 (5) ( ) 5 5 1 (	4 (5) (1) 5 5) (	+25°C			±1	
lı	leakage current	$V_I = V_{CCA}$ or GND	1.65V to 5.5V	1.65V to 5.5V	Full			±2	μΑ
			0)/	0)/+- 5 5)/	+25°C			±1	
1	A or D Dort	\\\. \\. \\. \\. \\. \\. \\. \\. \\. \\	0V	0V to 5.5V	Full			±2	μΑ
l <sub>off</sub>	A or B Port	$V_1$ or $V_0 = 0$ to 5.5V	0V to 5.5V	0V	+25°C			±1	^
			00 10 3.30	OV	Full			±2	μΑ
		CND (5)	1.65V to 5.5V	1.65V to 5.5V	Full			3	
$I_{CCA}$	V <sub>CCA</sub> supply current	$V_I = V_{CCA} \text{ or GND}^{(5)}$ $I_O = 0$	5V	OV	Full			2	μΑ
	carrent	10 0	OV	5V	Full			-2	μπ
		CND (5)	1.65V to 5.5V	1.65V to 5.5V	Full			3	
Іссв	V <sub>CCB</sub> supply current	$V_1 = V_{CCA}$ or GND <sup>(5)</sup> $I_0 = 0$	5V	OV	Full			-2	μΑ
		10 0	0V	5V	Full			2	
Icca + Iccb	Combined supply current	$V_1 = V_{CCA}$ or GND $I_0 = 0$	1.65V to 5.5V	1.65V to 5.5V	Full			4	μА
ΔΙ <sub>ССА</sub>	A port	One A port at V <sub>CCA</sub> – 0.6 V, B port = open	3V to 5.5V	3V to 5.5V	Full			50	μΑ
ΔІссв	B port	One B port at V <sub>CCB</sub> – 0.6 V, A port = open	3V to 5.5V	3V to 5.5V	Full			50	μΑ
Cı	Input capacitance	V <sub>I</sub> = GND or V <sub>CCA</sub>	0V to 3.6V	0V to 3.6V	+25°C		8.5		pF
Cıo	Output capacitance	Vo=GND ted with the input port.	0V to 3.6V	0V to 3.6V	+25°C		8.5		рF

<sup>(1)</sup>  $V_{\text{\tiny CCA}}$  is the  $V_{\text{\tiny CC}}$  associated with the input port.

<sup>(2)</sup>  $V_{CCB}$  is the  $V_{CC}$  associated with the output port.

<sup>(3)</sup> Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

<sup>(4)</sup> Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

<sup>(5)</sup> Hold all unused data inputs of the device at V<sub>CCI</sub> or GND to assure proper device operation.



# 8.5 Timing Requirements

# 8.5.1 V<sub>CCA</sub>=1.8V± 0.15 V

over recommended operating free-air temperature range, Full=-40°C to 125°C.

PARAMETER	FROM	то	TEMP		V <sub>CCB</sub> =1.8V ±0.15V (1)				=2.5V 2V <sup>(1)</sup>		=3.3V 3V <sup>(1)</sup>		₃=5V 5V <sup>(1)</sup>	UNIT
TAIGNIETER	(INPUT)	(OUTPUT)		MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX			
t <sub>PLH</sub>	۸	D	E. II	3.5	24.6	2.6	17.2	2	18.6	1.6	18.5			
$t_{PHL}$	Α	Α	В	Full	3.3	18.4	2.6	15.5	2.1	16.4	2	16.8	ns	

<sup>(1)</sup> This parameter is ensured by design and/or characterization and is not tested in production.

## 8.5.2 V<sub>CCA</sub>=2.5V± 0.2 V

over recommended operating free-air temperature range, Full=-40°C to 125°C.

PARAMETER	FROM	то	TEMP		=1.8V 5V <sup>(1)</sup>	V <sub>CCB</sub> =2.5V ±0.2V (1)		V <sub>CCB</sub> =3.3V ±0.3V (1)		V <sub>CCB</sub> =5V ±0.5V (1)		UNIT	
	(INPUT)	(OUTPUT)		MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	J	
t <sub>PLH</sub>	_	D	E. II	2.7	24.5	1.8	16.5	1.5	15.4	1.3	16.6		
t <sub>PHL</sub>	Α	Α	В	Full	2.5	18.4	1.6	12.7	1.5	11.3	1	10.4	ns

<sup>(1)</sup> This parameter is ensured by design and/or characterization and is not tested in production.

### 8.5.3 V<sub>CCA</sub>=3.3V± 0.3 V

over recommended operating free-air temperature range, Full=-40°C to 125°C.

PARAMETEI	FROM	то	TEMP				V <sub>CCB</sub> =2.5V ±0.2V <sup>(1)</sup>		V <sub>CCB</sub> =3.3V ±0.3V (1)		V <sub>CCB</sub> =5V ±0.5V (1)	
TAIGHTEIL	(INPUT)	(OUTPUT)		MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	UNIT
t <sub>PLH</sub>	^	В	Full	2.5	23.2	1.6	16.2	8.0	15.2	0.8	15.1	ns
t <sub>PHL</sub>	A	D	Full	2.4	15.3	1.5	12.5	0.9	10.2	8.0	10	ns

<sup>(1)</sup> This parameter is ensured by design and/or characterization and is not tested in production.

### $8.5.4 V_{CCA} = 5V \pm 0.5 V$

over recommended operating free-air temperature range, Full=-40°C to 125°C.

PARAMETER	FROM	то	TEMP		V <sub>CCB</sub> =1.8V ±0.15V (1)				V <sub>CCB</sub> =2.5V V <sub>CCB</sub> =3 ±0.2V (1) ±0.3V					UNIT
	(INPUT)	(OUTPUT)		MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	O.W.		
t <sub>PLH</sub>	۸	D	Full	2.2	23.2	1.2	16.2	0.7	15.8	0.6	15.2	ns		
t <sub>PHL</sub>	А	В	B Full	2.1	14.1	1	11.4	8.0	10.4	0.6	9.5	ns		

<sup>(1)</sup> This parameter is ensured by design and/or characterization and is not tested in production.

# 8.6 Operating Characteristics

T<sub>A</sub>=25°C

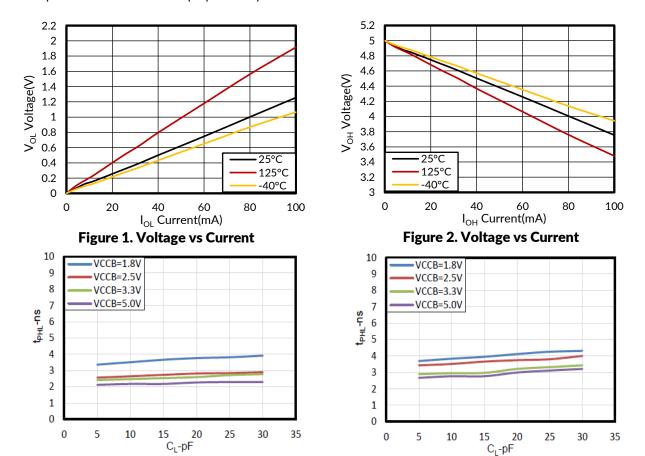
PARAMETER (1)		CONDITIONS	V <sub>CCA</sub> =V <sub>CCB</sub> =1.8V	V <sub>CCA</sub> =V <sub>CCB</sub> =2.5V	V <sub>CCA</sub> =V <sub>CCB</sub> =3.3V	V <sub>CCA</sub> =V <sub>CCB</sub> =5V	UNIT
			TYP	TYP	TYP	TYP	
C <sub>pdA</sub>	A-port input, B-port output	$C_L=0,$ f=10MHz, $t_r=t_f=5ns$	3	4	6	9	pF
C <sub>pdB</sub>	A-port input, B-port output	$C_L=0,$ f=10MHz, $t_r=t_f=5ns$	14	16	21	32	pF

<sup>(1)</sup> Power dissipation capacitance per transceiver.



# 8.7 Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.



T<sub>A</sub> = 25°C, V<sub>CCA</sub> = 1.8 V Figure 3. Typical Propagation Delay of High-to-Low (A to B) vs Load Capacitance

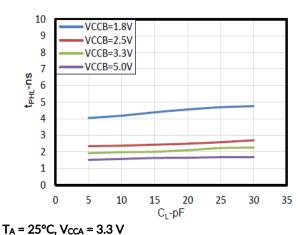
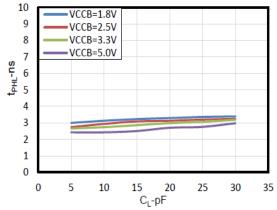


Figure 5. Typical Propagation Delay High-to-Low
(A to B) vs Load Capacitance

T<sub>A</sub> = 25°C, V<sub>CCA</sub> = 2.5 V Figure 4. Typical Propagation Delay High-to-Low (A to B) vs Load Capacitance

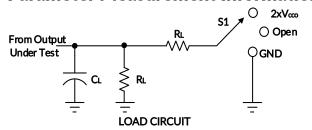


 $T_A = 25$ °C,  $V_{CCA} = 5$  V

Figure 6. Typical Propagation Delay High-to-Low (A to B) vs Load Capacitance

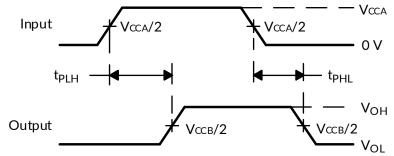


# **9 Parameter Measurement Information**



TEST	<b>S1</b>			
$t_{pd}$	Open			

<b>V</b> cc	CL	RL	V <sub>TP</sub>	
1.8V±0.15V	15pF	2kΩ	0.15V	
2.5V±0.2V	15pF	2kΩ	0.15V	
3.3V±0.3V	15pF	2kΩ	0.3V	
5V±0.5V	5V±0.5V 15pF		0.3V	



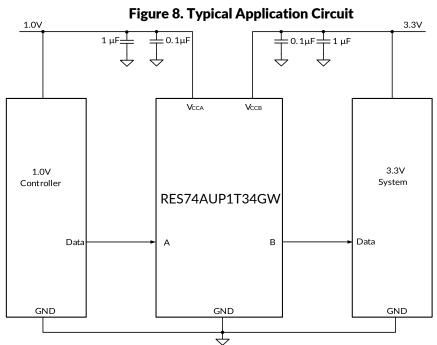
VOLTAGE WAVEFORMS, PROPAGATION DELAY TIMES

NOTES: A. C<sub>L</sub> includes probe and jig capacitance. Figure 7. Load Circuit and Voltage Waveforms

- B. All input pulses are supplied by generators having the following characteristics:  $PRR \le 10 \text{ MHz}$ ,  $Z_0 = 50\Omega$ ,  $dv/dt \ge 1V/ns$ .
- C. The outputs are measured one at a time, with one transition per measurement.
- D.  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{pd}}$ .
- E.  $V_{\text{CCA}}$  is the  $V_{\text{CC}}$  associated with the input port.
- F. All parameters and waveforms are not applicable to all devices.

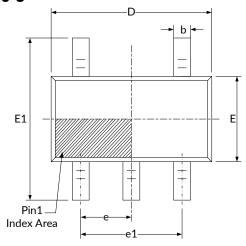
# **10 Application Information**

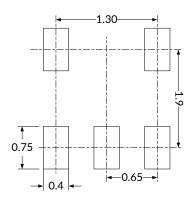
The RES74AUP1T34GW can be used in level-translation applications for interfacing devices or systems operating at different interface voltages with one another.



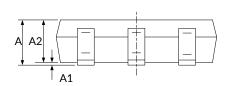


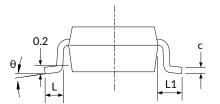
# SC70-5 (3)





# **RECOMMENDED LAND PATTERN (Unit: mm)**





Complete	Dimensions I	n Millimeters	Dimensions In Inches			
Symbol	Min Max		Min	Max		
A (1)	0.900	1.100	0.035	0.043		
A1	0.000	0.100	0.000	0.004		
A2	0.900	1.000	0.035	0.039		
b	0.150	0.350	0.006	0.014		
С	c 0.080		0.003	0.006		
D <sup>(1)</sup>	2.000	2.200	0.079	0.087		
E (1)	1.150	1.350	0.045	0.053		
E1	2.150	2.450	0.085	0.096		
е	0.650 (	BSC) (2)	0.026 (BSC) (2)			
e1	1.300 (BSC) (2)		0.051 (BSC) (2)			
L	L 0.260		0.010	0.018		
L1	0.5	0.525		21		
θ	0°	8°	0°	8°		

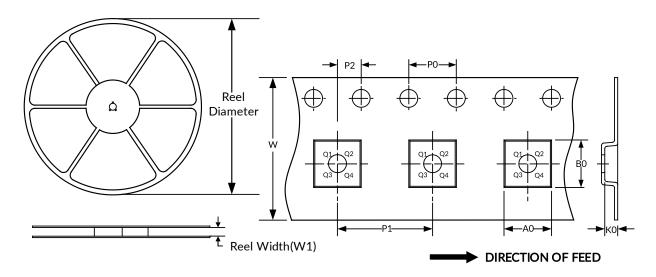
### NOTE:

- 1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
- 2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
- 3. This drawing is subject to change without notice.



# 12 TAPE AND REEL INFORMATION REEL DIMENSIONS

### **TAPE DIMENSION**



NOTE: The picture is only for reference. Please make the object as the standard.

### **KEY PARAMETER LIST OF TAPE AND REEL**

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-5	7"	9.5	2.25	2.55	1.20	4.0	4.0	2.0	8.0	Q3

#### NOTE:

- 1. All dimensions are nominal.
- 2. Plastic or metal protrusions of 0.15mm maximum per side are not included.